



Docket No.: N9450.0019/P019  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Shinichi Komura et al.

Allowed: November 22, 2004

Application No.: 09/876,119

Confirmation No.: 1245

Filed: June 8, 2001

Art Unit: 2675

For: **DISPLAY METHOD AND DISPLAY  
APPARATUS THEREFOR**

Examiner: P. A. Bell

**COMMENTS ON EXAMINER'S  
STATEMENT OF REASONS FOR ALLOWANCE**

U.S. Patent and Trademark Office  
Customer Window, Mail Stop Issue Fee  
Randolph Building  
Alexandria, VA 22314

Dear Sir:

Applicants agree in part with the Examiner's statement for allowance contained in the Office Action of November 22, 2004 in that the prior art of record fails to disclose the limitations noted by the Examiner. However, the Examiner's statement is not commensurate with the scope of the claims. For example, claim 7 recites a display apparatus comprising, *inter alia*, "an XY calculating circuit . . .; a signal comparator . . .; n-gradation approximation calculating circuit for dividing the pixels into pixel blocks of N rows x N' columns, and converting the gradation level of each pixel of each block into n-gradation approximation picture signal approximated to n values less than N x N'; and a signal control circuit for controlling the X driver, the Y driver, and liquid crystal drive voltage supplying circuit, according to the n-gradation approximation picture signal."

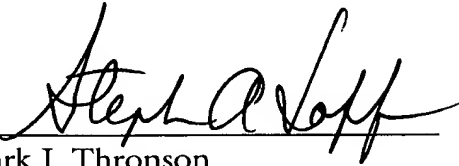
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The totality of the language of each claim provides additional reasons for the allowance of each claim.

Dated: February 22, 2005

Respectfully submitted,

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